ANALOG HARDWARE DESIGN CONTEST

Get ready to apply your lab knowledge in building exciting hardware design solutions with

TI's **ANALOG** SYSTEM LAB KIT PRO



Venue: EE Department, IIT Tirupati

Date: 8 March' 25 Time: 0900 - 1300

Also tune in for an interesting talk on Analog circuits in communication systems

by

Ashwin R

Time: 1700 - 1800 Date: 7 March' 25





